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VIA FACSIMILE - 08 PAGES (Including This Cover)

Commissioner for Patents
MAIL STOP: Amendment
POB 1450, Alexandria, Virginia 22313-1450
TRANSMITTED TO USPTO MAIN FACSIMILE NO. 703-872-9306

RE: Toshiyuki ARAI *et al.*, US Appl'n No. 10/085,063
Att'y Docket 501.41261X00
Examiner B.X. Tran - Group AU 1765 - USPTO Conf. No. 9140

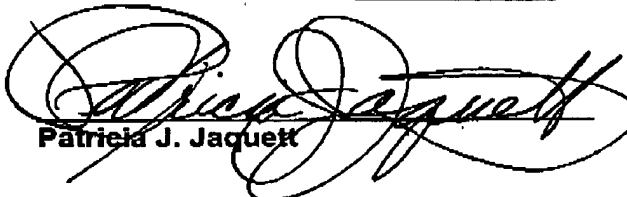
SUBMISSION OF AMENDMENT/PETITION/PTO-2038

Sir:

Applicant hereby submits the attached AMENDMENT (5 pages), PETITION FOR EXTENSION OF TIME (1 page) and FORM PTO-2038 (1 page) for entry in the above-noted application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached AMENDMENT (5 pages), PETITION FOR EXTENSION OF TIME (1 page) and FORM PTO-2038 (1 page) are being FORMALLY TRANSMITTED via the USPTO Main Facsimile No. 703-872-9306 on 17 September 2004.


Patricia J. Jaquett

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SEP 17 2004

501.41261X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshiyuki ARAI *et al.*
Application No.: 10/085,063
Filed: March 1, 2002
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 1765
Examiner: B.X. Tran
Conf. No.: 9140

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

17 September 2004

Sir:

In response to the Office Action mailed 17 May 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the Amendments.